

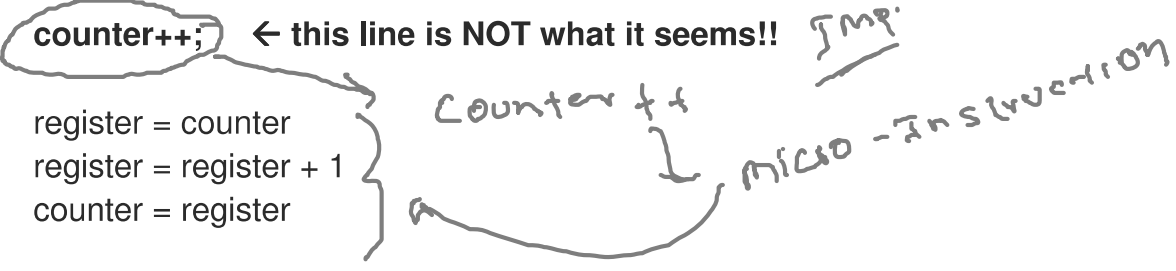
5/20/24

# PROCESS SYNCHRONIZATION

## The Producer Consumer Problem

Note that

is really -->



At a micro level, the following scenario could occur using this code:

T0;	Producer	Execute	register1 = counter	register1 = 5
T1;	Producer	Execute	register1 = register1 + 1	register1 = 6
T2;	Consumer	Execute	register2 = counter	register2 = 5
T3;	Consumer	Execute	register2 = register2 - 1	register2 = 4
T4;	Producer	Execute	counter = register1	counter = 6
T5;	Consumer	Execute	counter = register2	counter = 4

Process Synchronization

# PROCESS SYNCHRONIZATION

## Critical Sections

A section of code, common to  $n$  cooperating processes, in which the processes may be accessing common variables.

A Critical Section Environment contains:

- |                          |   |
|--------------------------|---|
| <b>Entry Section</b>     | Code <u>requesting entry into</u> the critical section.                   |
| <b>Critical Section</b>  | Code in which only <u>one process can execute at any one time</u> .       |
| <b>Exit Section</b>      | The end of the critical section, <u>releasing or allowing others in</u> . |
| <b>Remainder Section</b> | Rest of the code <u>AFTER</u> the critical section.                       |

### Process Synchronization

# PROCESS SYNCHRONIZATION

## Critical Sections

The critical section must **ENFORCE ALL THREE** of the following rules:

**Mutual Exclusion:** No more than one process can execute in its critical section at one time.

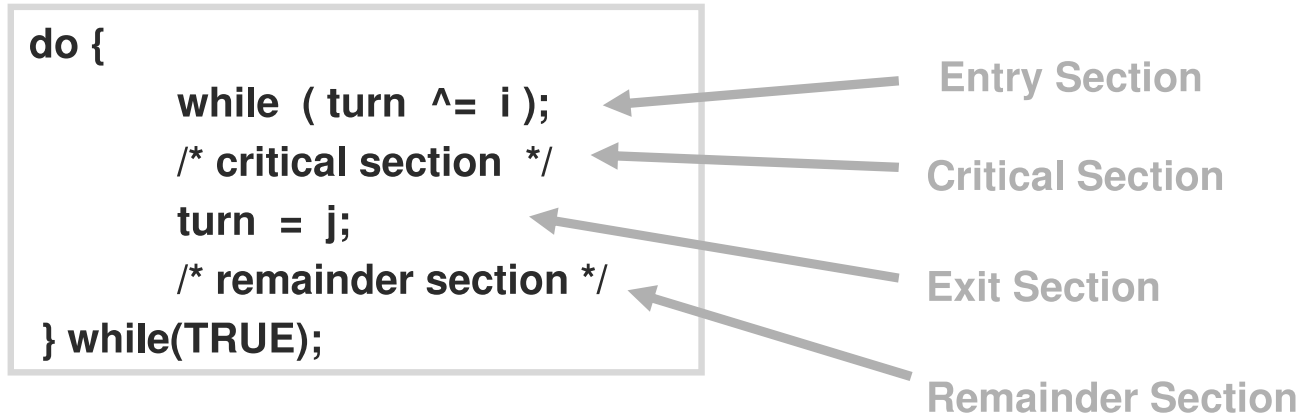
**Progress:** If no one is in the critical section and someone wants in, then those processes not in their remainder section must be able to decide in a finite time who should go in.

**Bounded Wait:** All requesters must eventually be let into the critical section.

# PROCESS SYNCHRONIZATION

## Two Processes Software

Here's an example of a simple piece of code containing the components required in a critical section.



Process Synchronization

# PROCESS SYNCHRONIZATION

## Two Processes Software

Here we try a succession of increasingly complicated solutions to the problem of creating valid entry sections.

NOTE: In all examples, *i* is the current process, *j* the "other" process. In these examples, envision the same code running on two processors at the same time.

### TOGGLED ACCESS:

```
do {  
    while ( turn ^= i );  
    /* critical section */  
    turn = j;  
    /* remainder section */  
} while(TRUE);
```

Algorithm 1

Are the three Critical Section  
Requirements Met?